

10971 U.S. PTO
10/047017



01/15/02

765/579

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10047017	FILING DATE 01/15/2002	CLASS 333	SUBCLASS	GAU 2817	EXAMINER <i>[Signature]</i> TAKAOKA, Dean
**APPLICANTS: Nyberg Petri;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 4925-149	
TITLE : Circuit topology for attenuator and switch circuits					

U.S. DEPT. OF COMM./PAT. & TM. PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED		
				Total Claims	Print Claim for O.G.	
ISSUE FEE		Primary Examiner		DRAWING		
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner		
		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM